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






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





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 Gonzalez, A.; Valero, M.; Gonzalez, J.; Monreal, T.;
[High Performance Computing, 1997. Proceedings. Fourth International Conference on](#)
 18-21 Dec. 1997 Page(s):364 - 369
 Digital Object Identifier 10.1109/HIPC.1997.634516
[AbstractPlus](#) | Full Text: [PDF](#)(572 KB) IEEE CNF
[Rights and Permissions](#)
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 Slama, Y.; Jemni, M.; Feautrier, P.;
[Computer Systems and Applications, 2003. Book of Abstracts. ACS/IEEE International Conference](#)
 14-18 July 2003 Page(s):91
 Digital Object Identifier 10.1109/AICCSA.2003.1227523
[AbstractPlus](#) | Full Text: [PDF](#)(160 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **3. Mapping instruction sequences onto EPOM-processor arrays: a framework for parallel data**
 Theis, J.-P.; Schlimper, H.;
[High Performance Computing, 1996. HIPC '96. 5th International Conference On](#)
 17-20 Dec. 1996 Page(s):105 - 113
 Digital Object Identifier 10.1109/HIPC.1996.737977
[AbstractPlus](#) | Full Text: [PDF](#)(440 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **4. Load balancing of DOALL loops in the Perfect Club**
 Elsesser, G.; Ngo, V.; Bhattacharya, S.; Tsai, W.T.;
[Parallel Processing Symposium, 1993. Proceedings of Seventh International](#)
 13-16 April 1993 Page(s):129 - 133
 Digital Object Identifier 10.1109/IPPS.1993.262868
[AbstractPlus](#) | Full Text: [PDF](#)(408 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **5. A Distributed Processing Network Architecture for Reconfigurable Computing**
 Vallina, F.M.; Oruklu, E.; Saniie, J.;
[Electro Information Technology, 2005 IEEE International Conference on](#)
 22-25 May 2005 Page(s):1 - 6

[AbstractPlus](#) | Full Text: [PDF](#)(176 KB) IEEE CNF

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-  **6. Graph partitioning for parallel applications in heterogeneous Grid environments**
 Kumar, S.; Das, S.K.; Biswas, R.;
[Parallel and Distributed Processing Symposium. Proceedings International IPDPS 2002 Abstract](#)
 15-19 April 2002 Page(s):66 - 72
 Digital Object Identifier 10.1109/IPDPS.2002.1015564
[AbstractPlus](#) | Full Text: [PDF](#)(296 KB) IEEE CNF
[Rights and Permissions](#)
-  **7. Improving processor allocation through run-time measured efficiency**
 Corbalan, J.; Labarta, J.;
[Parallel and Distributed Processing Symposium. Proceedings 15th International](#)
 23-27 April 2001 Page(s):6 pp.
 Digital Object Identifier 10.1109/IPDPS.2001.925021
[AbstractPlus](#) | Full Text: [PDF](#)(48 KB) IEEE CNF
[Rights and Permissions](#)
-  **8. Multiple vision agents navigating a mobile robot in a real world**
 Ishiguro, H.; Kato, K.; Tsuji, S.;
[Robotics and Automation, 1993. Proceedings, 1993 IEEE International Conference on](#)
 2-6 May 1993 Page(s):772 - 777 vol.1
 Digital Object Identifier 10.1109/ROBOT.1993.292071
[AbstractPlus](#) | Full Text: [PDF](#)(492 KB) IEEE CNF
[Rights and Permissions](#)
-  **9. Hardware/software partitioning of multirate system using static scheduling theory**
 Kamdem, R.; Fonkoua, A.; Zenatti, A.;
[Computer Design, 1999. \(ICCD '99\) International Conference on](#)
 10-13 Oct. 1999 Page(s):640 - 645
 Digital Object Identifier 10.1109/ICCD.1999.808609
[AbstractPlus](#) | Full Text: [PDF](#)(208 KB) IEEE CNF
[Rights and Permissions](#)
-  **10. Incorporating the parallel processing techniques with the demand-driven model of functional languages**
 Hong Shen; Kobayashi, H.; Nakamura, T.;
[TENCON '93. Proceedings. Computer, Communication, Control and Power Engineering, 1993 IEEE Conference on](#)
 Issue 0, Part 10000, 19-21 Oct. 1993 Page(s):146 - 149 vol.1
 Digital Object Identifier 10.1109/TENCON.1993.319949
[AbstractPlus](#) | Full Text: [PDF](#)(256 KB) IEEE CNF
[Rights and Permissions](#)
-  **11. Compiler techniques for the distribution of data and computation**
 Navarro, A.; Zapata, E.; Padua, D.;
[Parallel and Distributed Systems, IEEE Transactions on](#)
 Volume 14, Issue 6, June 2003 Page(s):545 - 562
 Digital Object Identifier 10.1109/TPDS.2003.1206503
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(2965 KB) IEEE JNL
[Rights and Permissions](#)
-  **12. Towards predicting real-time properties of a component assembly**
 Bondarev, E.; de With, P.; Chaudron, M.;
[Euromicro Conference, 2004. Proceedings, 30th](#)
 2004 Page(s):601 - 610
 Digital Object Identifier 10.1109/EURMIC.2004.1333428
[AbstractPlus](#) | Full Text: [PDF](#)(534 KB) IEEE CNF
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-  **13. Resource estimation and task scheduling for multithreaded reconfigurable architectures**
Sudarsanam, A.; Srinivasan, M.; Panchanathan, S.;
[Parallel and Distributed Systems, 2004. ICPADS 2004. Proceedings. Tenth International Conference](#)
7-9 July 2004 Page(s):323 - 330
Digital Object Identifier 10.1109/ICPADS.2004.1316111
[AbstractPlus](#) | Full Text: [PDF](#)(323 KB) | [IEEE CNF](#)
[Rights and Permissions](#)
-  **14. Adaptive distributed execution of Java applications**
Felea, V.; Tournel, B.;
[Parallel, Distributed and Network-Based Processing, 2004. Proceedings. 12th Euromicro Conference](#)
11-13 Feb. 2004 Page(s):16 - 21
Digital Object Identifier 10.1109/EMPDP.2004.1271421
[AbstractPlus](#) | Full Text: [PDF](#)(300 KB) | [IEEE CNF](#)
[Rights and Permissions](#)
-  **15. Memory binding for performance optimization of control-flow intensive behaviors**
Khouri, K.S.; Lakshminarayana, G.; Jha, N.K.;
[Computer-Aided Design, 1999. Digest of Technical Papers. 1999 IEEE/ACM International Conference](#)
7-11 Nov. 1999 Page(s):482 - 488
Digital Object Identifier 10.1109/ICCAD.1999.810698
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-  **16. On the use of pseudorandom sequences for high speed resource allocators in superscalar processors**
Srinivasan, S.; John, L.K.;
[Computer Design, 1999. \(ICCD '99\) International Conference on](#)
10-13 Oct. 1999 Page(s):124 - 130
Digital Object Identifier 10.1109/ICCD.1999.808416
[AbstractPlus](#) | Full Text: [PDF](#)(128 KB) | [IEEE CNF](#)
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-  **17. "No-Compile-Time Knowledge" distribution of finite element computations on multiprocessors**
Erhel, J.; Hahad, M.; Priol, T.;
[System Sciences, 1996. Proceedings of the Twenty-Ninth Hawaii International Conference on](#)
Volume 1, 3-6 Jan. 1996 Page(s):614 - 615 vol.1
Digital Object Identifier 10.1109/HICSS.1996.495512
[AbstractPlus](#) | Full Text: [PDF](#)(160 KB) | [IEEE CNF](#)
[Rights and Permissions](#)
-  **18. Balancing load versus decreasing communication: exploring the tradeoffs**
Taylor, V.E.; Holmer, B.K.; Schwabe, E.J.; Hribar, M.R.;
[System Sciences, 1996. Proceedings of the Twenty-Ninth Hawaii International Conference on](#)
Volume 1, 3-6 Jan. 1996 Page(s):585 - 593 vol.1
Digital Object Identifier 10.1109/HICSS.1996.495509
[AbstractPlus](#) | Full Text: [PDF](#)(708 KB) | [IEEE CNF](#)
[Rights and Permissions](#)
-  **19. A simulated annealing approach for high-level synthesis with reconfigurable functional units**
Alves, J.C.; Matos, J.S.;
[Circuits and Systems, 1995. Proceedings. Proceedings of the 38th Midwest Symposium on](#)
Volume 1, 13-16 Aug. 1995 Page(s):314 - 317 vol.1
Digital Object Identifier 10.1109/MWSCAS.1995.504440
[AbstractPlus](#) | Full Text: [PDF](#)(324 KB) | [IEEE CNF](#)
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-  **20. Predicting the performance of scientific applications on distributed memory multiprocessors**

Abramson, D.;

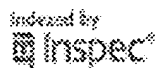
Scalable High-Performance Computing Conference, 1994. Proceedings of the

23-25 May 1994 Page(s):285 - 292

Digital Object Identifier 10.1109/SHPCC.1994.296656

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